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Application Number	10/773,968
Filing Date	February 6, 2004
First Named Inventor	Paul D. Shirley
Art Unit	
Examiner Name	
Attorney Docket Number	MIO 0112 PA/40509.272/00-0265

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		MICHAEL QUIRK AND JULIAN SERDA, Semiconductor Manufacturing Technology, November 11, 2003, Pages 357 - 360 Chapter 13, Prentice Hall, Upper Saddle River, New Jersey and Columbus, Ohio	

Examiner Signature		Date Considered	1/08
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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